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QUALIFCATIONS:

- Ph. D. Degree Holder.
- 16 years in teaching in Jordan and doing research in USA.
- High skills on teacher training and professional development.
- 25 years previous successful experience managing and providing technical, administrative and financial guidance within international projects in capacity building and training.
- Strong experience in strategic planning, SWOT Analysis, socio-economic analysis, challenges and opportunities, managing and directing successful projects for more than 7 years in Jordan.
- Excellent oral and written communication skills and strong interpersonal skills.
- Fluent in English, Arabic.

PROFESSIONAL EXPERIENCE:

- Dec. 2002 - 20110: Assistant Prof., Department of Computer Engineering, Jordan University of Science & Technology, Jordan,
- Feb. 2010 – Currently: Associate Prof. Department of Computer Engineering, Jordan University of Science & Technology, Jordan,
- Sept. 94- June 95: Teaching and Research Assistance, Dept. of Computer Engineering, Patras University, Greece.
- Aug. 92- Sept. 93: Developer, CAD Lab, Dept. of Computer Engineering, Patras, Greece.
- Developed and maintained CAD/CAM Application, using CADENCE, and Mintor Graphics.
- Aug. 95 - Aug. 2000: Teaching and Research Assistance, Dept. of Computer and Electrical Engineering, Patras, Greece.

PROFFESIONAL ACTIVITIES AND AWARDS

- ✓ Most Download Paper's,
- ✓ 4th Place in Contest "Static timing Analysis," Mardird, Spain 2011,
- ✓ Budget amount 35 Euro, PCI- Mediterraneo, Spain – Jordan Resarch Program.
- ✓ 500,000 Euro, FP7, EU JEWEL Project, (2010 - 2013)
- ✓ 750,000 Euro, FP7, EU MOSAIC Project (2014-2016)
- ✓ Best Reviewer Award, ACIT
- ✓ Most active reviewer in ACIT/2008
- ✓ Journal Reviewer: VLSI DESIGN: An International Journal of Custom-Chip Design.
- ✓ Conference Reviewer: IEEE ICECS, IEEE ISCAS, DCCA, ICIT, MobiMedia, ACIT.
- ✓ 10/89 – 12/2000 National Scholarship Foundation, GREECE (Undergraduate, Graduate).
- ✓ 06/2001-10/2001: Post Doc. Summer semester, University of Californai, at Davis.
- ✓ 07/2003-10/2003: Post Doc. Summer semester, University of Oldenburg, OFFIS, DAAD Program.
- ✓ Train of Trainees

A. VISITING PROFESSOR

1. 2014 – 2015, Fulbright Visiting Scholar at Purdue University, West lafayette – Indiana, USA
2. **Summer 2007, 2008:** Post Doc, IKY Scholraship, University of Patras Greece, Advisor, Prof. Odysseas Koufopavlou,
3. **Summer 2006**: Post Doc, University of Patras Greece, Advisor, Prof. Odysseas Koufopavlou
4. **Summer 2003, 2005:** Post Doc, DAAD Scholarship, Oldenburg University, Supervisor: Prof. N. wolfgange.
5. **Summer 2001:** Visting Scholar, University of California at Davis, Department of Electrical and Computer Engineering.

RESEARCH INTERESTS:

- Low Power Design in System Level.
- Nanoscale Modeling and Design
- Software Tools

PUBLICATIONS:

JOURNAL and Conferences PAPERS:

1. A.P. Kakarountas, H.E. Michail, and C.E. Goutis, A. Rjoub, " High-Throughput Implementation of the RipeMD-160," International Journal of Internet Technology and Secured Transactions, Vol. 1, Nos. 3/4, pp. 309-316, 2009
2. Abdoul Rjoub and Areej Ahmad, " Fast Modeling Technique for Nano Scale CMOS Inverter and Propagation Delay Estimation," Appeared in the 24th International Workshop on Power and Timing Modeling, Optimization and Simulation, Palma (Mallorca) 29 September to 1 October 2014.

3. Abdoul Rjoub, Nedal Taradeh and Mamoun Mistarihi, " Gate Leakage Current Accurate Models for Nanoscale MOSFET Transistors," Appeared in the 24th International Workshop on Power and Timing Modeling, Optimization and Simulation, Palma (Mallorca) 29 September to 1 October 2014.

4. **Abdoul Rjoub*i**, Hassan Almanasrah, "Low Leakage Power Sequential Circuits Using Multi-Vth at Nano-Scale Transistor," Journal of Energy and Power Engineering 7 (2013).

5. **Abdoul Rjoub**, Atheer Al-Shaggah and Mohammad Khasawneh, Carbon Nanotube Field effect Transistor Models Performance and Evaluation", IEEE International Applied and Electrical and Electronics Conference, Amman, December 2013, Jordan.

6. **Abdoul Rjoub**, Almanasrah Hassan, Shihab Kattab, "An efficient DELOTS Algorithm for low leakage current at nano-scale transistor", IEEE Conference on Applied Electrical Engineering and Computing Technologies (AEECT), December, 6-8 2011.

7. **Abdoul Rjoub**, Hassan Manasrah, Mutasem Ajlouni, "A Fast Input Vector Control Approach for Sub-threshold Leakage Power Reduction," International Conference of MELECON, 2011, Tunisia. Tunis.

8. **Abdoul Rjoub**, Mamoun Al-Mistarihi, Nedal Al-Taradeh, "Recent Transport Models in Nanoscale MOSFET Transistor – Study and Analysis," The 8th Jordanian International Electrical and Electronics Engineering Conference, Amman-Jordan, 16 – 18 April, 2013.

9. **Abdoul Rjoub**, Mamoun Al-Mistarihi, Nedal Al-Taradeh, "Transport Mobility and Injection Velocity Model for Nanoscale MOSFET Transistor," The 8th Jordanian International Electrical and Electronics Engineering Conference, Amman-Jordan, 16 – 18 April, 2013.

10. **Abdoul Rjoub**, Mamoun Mistarihi, Nedal Taradeh, "Backscattering Coefficients and Carrier Transport models for Nanoscal CNTFET Leakage Minimization, Under revision, *IEEE Transactions on Devices*.

11. **Abdoul Rjoub**, Mamoun Mistarihi, Nedal Taradeh, "Bascattering Coefficient Accurate Model for Nanoscale Si-MOSFET Transistor," IEEE Faible Tension Faible Consommation, June 20-21, 2013, Paris-France.

12. Abdoul Rjoub, Muna Aldourah, "The Performance and Behavioral of Dual Edge Triggered Flip-Flops in Nano-Technology," International Journal of Computer Aided Engineering and Technology, Vol. 4 No. 1, pp. 49-71.
13. Abdoul Rjoub, Nedal Al-Taradeh, "Accurate Modeling for CMOS Inverter Overshooting time in Nanoscale Paradigm," The 25th International Conference on Microelectronics (ICM 2013): Micro/Nanoelectronics, Lebanon.
14. **Abdoul Rjoub**, Nedal Al-Taradeh, Mamoun F. Al-Mistarihi, " Modeling in Nanoscale CMOS technology: Challenges and Design Requirements," Proceedings of the "International Conference on Solar energy for MENA region (INCOSOL)". Amman, Jordan, 22-23October 2012

15. **Abdoul Rjoub,** Nedal R. Al-Taradeh, and Mamoun F. Al-Mistarihi, "Accurate Subthreshold Leakage Model for Nanoscale MOSFET Transistor," Under Revision, IEEE Inernational Conference on Circuits and Systems, Abu-Dabhi, United Arab Emirates.
16. **Abdoul Rjoub,** Nedal R. Al-Taradeh, and Mamoun F. Al-Mistarihi, "Gate Leakage Current Accurate *Abstract*—Models for Nanoscale MOSFET Transistors, Under Revision, IEEE Inernational Conference on Circuits and Systems, Abu-Dabhi, United Arab Emirates.

17. Abdoul Rjoub, Nedal Taradeh and Mamoun Mistarihi, " Gate Leakage Current Accurate Models for Nanoscale MOSFET Transistors," Appeared in the 24th International Workshop on Power and Timing Modeling, Optimization and Simulation, Palma (Mallorca) 29 September to 1 October 2014.

18. Bara'h Tarawneh, Abdoul Rjoub, "Voltage Programmed Pixel Designs in AMOLED Display," Fourth International Conference on Information and Communication Systems (ICICS 2013), April 23-25, 2013. Irbid - Jordan

19. Bara'h Tarawneh, Abdoul. Rjoub, "A New Voltage Programmed Pixel Design Circuit for AMOLED Display," The 8th Jordanian International Electrical and Electronics Engineering Conference, Amman-Jordan, 16 – 18 April, 2013.

20. Calliope-Louisa Sotiropoulou, Christos Gentsos, Spiridon Nikolaidis and **Abdoul Rjoub**, "FPGA-based Canny Edge Detection for Real-Time Applications," 26th Conference on Design of Circuits and Integrated Systems (DCIS), Albufeira, Portugal, Nov. 2011

21. D Tzagkas, C. Varnavidou, I. Pappas, L. Voudouris, **A. Rjoub**, S. Nikolaidis, "Pass transistor driving RC loads in nanoscale technologies," IEEE Mediterranean Electrotechnical Conference (MELECON), Tunisia, March 26-28, 2012.

22. Dimitiros Tzagkas, Ilias Pappas, **Abdoul Rjoub**, Spyridon Nikolaidis, "Pass transistor driving CRC loads in nanoscale technologies," Journal on circuits, Systems and Computers, World Scientific Publishing Company.

23. Dimitrios Tzagkas, Spyridon Nikolaidis, **Abdoul Rjoub** "Estimating the Starting Point of Conduction in Nanoscale CMOS Gates," International Conference on Electronics, Circuits and Systems (ICECS), Seville, Spain, 9-12 Dec. 2012.

24. Jarrah, M. Shdefat, and A. Rjoub, "An Efficient Implementation of IRIS Recognition Algorithms for Portable Equipment," On proceeedings of Arab International Conference for Information Technology. November. 26-28, 2007, Syria.

25. L. Voudouris, **A. Rjoub**, S. Nikolaidis, "High speed FPGA implementation of Hough transform for real-time applications", IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS), Tallinn, Esthonia, April 18-20, 2012.
26. Mamoun Al-Mistarihi, Abdoul Rjoub, Nedal Al-Taradeh, "'A New Analytical Model of Drain Induced Barrier Lowering DIBL in Nanoscale Si-MOSFET," The 25th International Conference on Microelectronics (ICM 2013): Micro/Nanoelectronics, Lebanon.

27. P. Chaourani, I. Pappas, S. Nikolaidis, **A. Rjoub**, "Pass transistor operation modeling for nanoscale technologies,"International Workshop on Power and Timing Modeling, Optimization and Simulation (PATMOS), pp. 53-62, Madrid, Spain, Sept. 2011.

28. P. Chaourani, I. Pappas, S. Nikolaidis, A**. Rjoub**, "Pass transistor operation for rising ramps in both terminal inputs," 26th Conference on Design of Circuits and Integrated Systems (DCIS), Portugal, Nov. 2011

29. P. Souras, N. Sklavos, A. Rjoub, and C. Efstathiou, "Networks Security: Risk Management and Economics in Information Technology." On Proceeding of Arab Conference for Information Technology, pp. 178-183, 2006.

30. Panagiotis Chaourani, Spyridon Nikolaidis, **Abdoul Rjoub**, "Modelling the pass transistor charging operation for nanoscale technologies", Integration, The VLSI Journal, Elselvier.

31. Panagiotis Chaourani, Spyridon Nikolaidis, **Abdoul Rjoub**, "Subthreshold influence on Pass Transistor Operation Modeling for Nano-Scale Technologies," The 8th Jordanian International Electrical & Electronics Engineering Conference, Amman, 16-18 April 2013.

32. Pappas, L. Voudouris, S. Nikolaidis, **A. Rjoub**, "A new current – programmed pixel design for AMOLED displays implemented with organic thin film transistors," International Conference on Microelectronics, MIEL 2012 Conference, Nis, Serbia, May 13-16, 2012.

33. Rjoub A. Ajlouni, " An Efficient Approach to Calculate Leakage Current Based on SPICEs Parameters at CMOS Transistors," On On proceeedings of International Conference on Information Technology, May 9-11, 2007, Jordan.

34. Rjoub and Ali Shatnawi, "High Speed Low Power Multi-threshold Voltage Flip Flops". IEE Journal of Electrical Engineering, Vol. 54, No. 5-6, 2003, pp. 123-127.

35. Rjoub and O. Koufopavlou, " Multiple Low Swing Voltage Values for CPL, CVSL AND Domino Logic Families," in proc. IEEE International Conference on Electronics Circuits and Systems, vol. I, pp. 903-906, Junieh, Lebanon, December 17-20, 2000.

36. Rjoub and O. Koufopavlou, "Efficient Low Power/Low Swing Bus Design Architectures," Journal of VLSI Design, No. 3, Vol. 12, 2001, pp. 415 – 429.7100483

37. Rjoub and O. Koufopavlou, "Low Power High Speed Multithreshold Voltage CMOS Bus Architectures", Computers & Electrical Engineering an International Journal, Volume 30, 2004, pp. 269 - 280.

38. Rjoub and O. Koufopavlou, "Multithreshold Voltage Low Swing/Low Voltage Techniques in Logic Gates," Integration, the VLSI Journal, Volume 38, 2004, pp. 283-298.

39. Rjoub and O. Koufopavlou, "Multithreshold Voltage Technology for Low Power Bus Architecture", Tenth International Conference on Very Large Scale Integration, pp. 219 – 232, Lisboan, Portugal, December 1 - 4, 1999.

40. Rjoub, B. Tall, L. Mardeeni, & R. Sharou, "A Novel Multi-Forms Multiple Choice Editor Exam Tool Based on HTML Website, On the proceedings of the 7th International Conference on Information Technology Based Higher Education and Training, pp. 854-869, *10 – 13July, 2006, Sydney, Australia,*

41. Rjoub, L. Bisdounis, O. Koufopavlou, "Influence of the nMOS and pMOS Transistor Threshold Voltage on CMOS Circuits Power Dissipation", in proc. IEEE International Conference on Electronics Circuits and Systems, vol. II, pp. 545-549, Cairo, Egypt, December, 1997.

42. Rjoub, L. Tawalbeh, "A Low Power, High Frequency and High Precision Multiplier Architecture in GF(p). on Proceedings of International e-Conference of Computer Science, 2005.

43. Rjoub, M. Alrousan, O. Aljarrah, O. Koufopavlou, "An Efficient Low - Swing Multithreshold Voltage Low Power Design Technique", Journal of circuits Systems and Computer, Volume 13, No. 1, February 2004, pp. 193-203.

44. Rjoub, M. Al-Rousan, O. Jarrah, and O. Koufopavlou, " Multi-Level Low Swing Voltage Values for Low Power Design Applications", International Symposium in Circuits and Systems, Vol. IV, pp. 590-593, Sydney, Australia, 30May-2June, 2001.

45. Rjoub, M. Athamneh, "The Influence of the Nanometer Technology on Performance of CPL Full Adders," Vol. 4, No. 9, September 2009, Academy Publisher, Finland.

46. Rjoub, M. Musameh, O. Koufopavlou, " An Optimal Low-Power/High Performance DDP-based Corba-H64 Cipher," On Proceedings of International Mobile Multimedia Communications Conference, August 27-29, 2007, Greece.

47. Rjoub, M. Musameh, O. Koufopavlou, " Full Custom Low-Power / High Perfromance DDP-Based COBRA-L64 Cipher, Submitted to: International Journal of Circuits, Systems, and Computers. Ref. No.: COMPELECENG-D-08-00331

48. Rjoub, O. Koufopavlou, "Efficient Drivers, Receivers, and Repeaters for Low Power CMOS Bus Architectures", in proc. IEEE International Conference on Electronics Circuits and Systems, vol. II, pp. 789-794, Paphos, Cyprus, September 5 –8, 1999.

49. Rjoub, O. Koufopavlou, "Low Power Methods Comparison", Proceedings of 3rd World Multi-conference on: Circuits, Systems, Communications and Computers, Athens, Greece, June 4 – 8, pp. 327 – 331, 1999.

50. Rjoub, O. Koufopavlou, "Low Voltage Swing Gates for Low Power Consumption", in Proc. IEEE International. Symposium on Circuits and Systems, vol. I, pp. 234 - 237, Florida, USA, May 30 - June 2, 1999.

51. Rjoub, O. Koufopavlou, "Low-Power Domino Logic Multiplier Using Low-swing Technique", in proc. IEEE International Conference on Electronics Circuits and Systems, vol. II, pp. 45- 48, Lisboan, Portugal, 1998.

52. Rjoub, O. Koufopavlou, "Low-swing/Low-power Driver Architecture", in proc. IEEE International Conference on Electronics Circuits and Systems, vol. II, pp. 639-642, Paphos, Cyprus, September, 5 - 8, 1999.

53. Rjoub, O. Koufopavlou, S. Nikolaidis, "Low-power/Low-swing Domino CMOS Logic", in Proc. IEEE International. Symposium on Circuits and Systems, vol. III, Monterey, USA, May 31 – June 3, 1998.

54. Rjoub, S. Nikolaidis, O. Koufopavlou, T. Stouraitis, " A New Efficient low Power Bus Architecture", in Proc. of IEEE International Symposium on Circuits and Systems, vol. III, pp. 1864-1867, Hung-Kung, China, June, 1997.

55. Rjoub, V. Paliouras, T. Stouraitis, "A Full-Custom Implementation of an RNS Multiplier", International Conference on Electronics Circuits and System, 95, pp. 25-28, Amman, Jordan, December, 1995.

Citations:

The above papers have been cited up to **178** times in journal and conference publications, as well as in patents, books, technical reports and dissertations. Some of a detailed list of his citations can be found bellow.

B. CO ORDINATIONS:

1. Coordinate a professional visit: professors from University of Patras-Greece to Jordan University of Science and Technology, December 2004.
2. Coordinate a Professional visit: Professors from University of Oldenburg-Germany to Jordan University of Science and Technology, February 2003.
3. Coordinate a Professional visit: Head of Administration Office, Oldenburg University, Ms. Brigit Bruns, 2006.

A. Courses designed at JUST:

- Hardware Design Language (HDL)

B. Laboratories Design at JUST

- Digital Logic Design Lab.
- Microprocessor Design Lab.

EXTRACURRICULAR ACTIVITIES

- International Computer Driving License (ICDL):

  - Examiner and Trainer.

MEMBERSHIP OF SCIENTIFIC AND PROFESSIONAL SOCIETIES

1. IEEE Member(2001-2002)
2. Technical Chamber of Jordan.
3. Computer Society of Jordan
4. Committee in Scientific Research Friendship (2002-2204)

ACTIVITIES & INTERNATIONAL PROJECTS:

- European Projects, TEMPUS

2003	Proposal to Create VLSI Design Laboratory in Jordan and Levant Region.
2004	Proposal to Create AirPollution Monitoring Laboratory in Jordan
2005	Proposal to Creat Training Center for IT Proposes
2006	Proposal to Creat Training Center for Fresh Graduated Students
2007	Proposal to Computerase the Solid Wast Landfills in the Region

LANGUAGES

Arabic:	Native
English:	Fluent
French:	Good,
Greek:	Fluent,
Italic:	Good.

REFERENCE (International)

1. Prof. Odysseas Koufopavlou
   VLSI Design Laboratory
   Patras University.
   Department of Electrical and Computer Engineering.
   odysseas@vlsi.ee.upatras.gr

2. Prof. Thanos Stouraitis
   VLSI Design Laboratory
   Patras University.
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