

1. Name:  
Khaled Mayyas
2. Education:  
Ph.D. Electrical Engineering, University of Ottawa, 1995.  
M. Sc. Electrical Engineering, Jordan University of Science and Technology (JUST), 1992.  
B.Sc. Electrical Engineering, JUST, 1990.
3. Academic experience:  
JUST, Professor of Electrical Engineering, 2006-present, Full Time.  
JUST, Associate Professor of Electrical Engineering, 2001-2006, Full Time.  
JUST, Assistant Professor of Electrical Engineering, 1996-2001, Full Time.  
JUST, Dean of Engineering, JUST, 2007-2011, Full Time.  
JUST, Vice Dean of Engineering, JUST, 2005-2007, Full Time.  
University of Ottawa, Visiting Research Associate, summers of 1996, 1997, 1998, 1999, 2001, Part Time  
Univ. of Philadelphia, Jordan, Lecturer, Dept. of Electrical Engineering, 1992-1993, Full Time  
Univ. of Ottawa, Teaching Assistant, 1993-1995, Part Time.
4. Non-academic experience:  
None.
5. Professional registrations:  
None.
6. Memberships in professional organizations:  
Jordan Assoc. Engineers, Jordan.
7. Honors and awards:  
Scholarship recipient from JUST/Canadian International Development Agency (CIDA) to pursue my Ph.D. studies at Ottawa University, Canada, 1993-1995.
8. Service activities:  
Dean of Engineering, JUST, 2007-present.  
Vice Dean of Engineering, JUST, 2005-2007.  
Member of the IEEE Signal Processing Technical Committee on Signal Processing Education, 2004-2005  
Technical Program Committee on Signal Processing Education, 2006 IEEE International Conference on Acoustics, Speech, and Signal Processing ICASSP'2006, France, , May 2006.  
Technical Program Committee, Wireless Euro-Mediterranean International Conference 2006, Jordan, Amman, 2006.

Technical Program Committee on Signal Processing Education, ICASSP'2005, USA, March 2005.

Technical Program Committee, The 5th IEEE International Symposium on Signal Processing and Information Technology ISSPIT'2005, Greece, 2005.

Technical Program Committee, The 4th IEEE International Symposium on Signal Processing and Information Technology ISSPIT'2004, Italy, 2004.

A session chair of Signal Processing for Communications (1), The 1st IEEE International Symposium on Signal Processing and Information Technology ISSPIT'2001, Cairo, 2001.

Technical Program Committee, The Fifth International Symposium on Signal Processing and its Applications, Brisbane, Australia, 1999.

Coordinating on behalf of JUST the establishment of Sirenada Research and Development Center at the JUST campus. Sirenada is a US-based company specializing in interactive radio technology.

Reviewer for the IEEE Transactions on Signal Processing, IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on Speech and Audio Processing, IEEE Signal Processing Letters, and Journal of Circuits, Systems, and Signal Processing.

Reviewer for several international conferences such as ISCAS'2002, ISSPA'2003, ISSPIT'2003, ISSPIT'2004, ICASSP'2005.

On several committees for the promotion of applicants from assistant to associate professorship and from associate to full professorship in the Faculty of Engineering at JUST.

9. Publications in the last five years:

``New Constant Modulus Blind Equalizer with Optimum Tap-length for QAM Signals'', S. Alsaid, E. Abdel-Raheem, ISSPIT'2019, Ajman, UAE, December 10-12, 2019.

``Variable Order Adaptation of LMS Lattice Predictors Using Forward Residual Errors'', S. Alsaid, E. Abdel-Raheem, 61st IEEE International Midwest Symposium on Circuits and Systems, Windsor, ON, Canada August, 2018.

``Variable tap-length algorithm using lattice structured LMS adaptive filters'', S. Alsaid, E. Abdel-Raheem, Mohammed Khalid, IEEE CCECE 2017, Windsor, ON, Canada, 30 April-3 May 2017.

``A New Variable Length NLMS Adaptive Algorithm'', H. A. AbuSeba, Digital Signal Processing, November 2014.

``Performance Analysis of the Selective Coefficient Update NLMS Algorithm in an Undermodeling Situation'', *Digital Signal Processing*, December 2013.

K. Mayyas, ``A Variable Step-Size Selective Partial Update LMS Algorithm'', *Digital Signal Processing*, Volume 23, Issue 1, Pages 75-85, January 2013.

10. Professional development activities:

Helping in surprising a Ph.D. student in the Electrical and Computer Engineering Dept., University of Windsor, Canada, who graduated in 2019, which resulted so far in the first three conference papers in the publications list.